

**PATENT APPLICATION**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of

Docket No: Q79257

Katsuki KUSUNOKI

Appln. No.: 10/581,335

Group Art Unit: 2829

Confirmation No.: 6238

Examiner: Shantanu Pathak

Filed: December 14, 2006

For: METHOD FOR PRODUCTION OF SEMICONDUCTOR CHIP, AND  
SEMICONDUCTOR CHIP

**AMENDMENT UNDER 37 C.F.R. § 1.111**

**MAIL STOP AMENDMENT**

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated June 24, 2009, please amend the above-identified application as follows on the accompanying pages. Applicants concurrently submit a Petition for Extension of Time for one month, extending the time for responding to the Office Action of June 24, 2009 to October 24, 2009, thus providing for timely filing by Monday, October 26, 2009 (October 24, 2009 falls on a Saturday).

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